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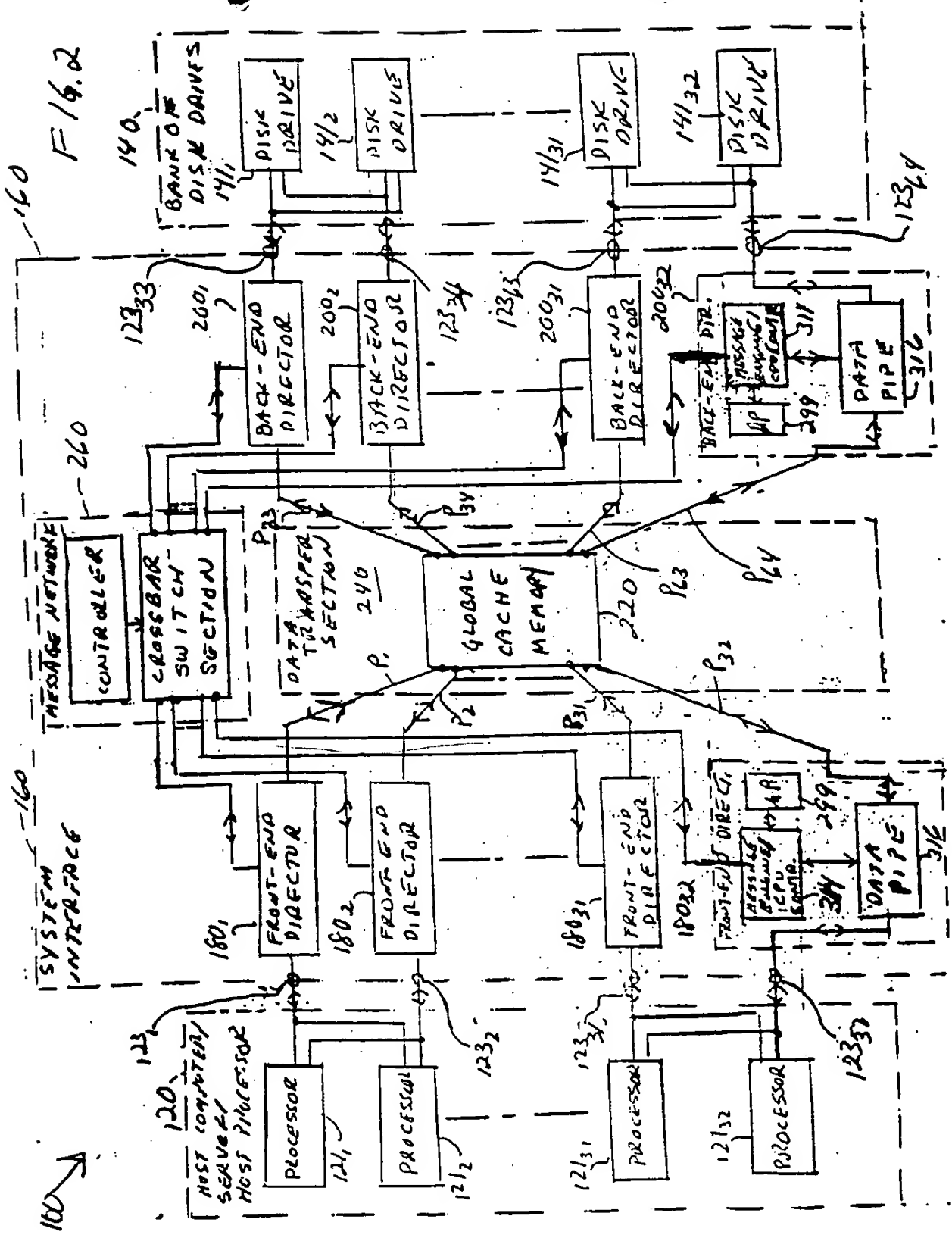
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002260-85072-000

FIG. 3

DIRECTOR BOARD

CACHE MEMORY 220

FIG. 4

BACKPLANE 302

MESSAGE NETWORK BOARD 304

MESSAGE NETWORK BOARD 3042

TO/FROM DISK DRIVES 140  
TO/FROM HOST COMPUTER / SERVER / HOST PROCESSOR 120

DIRECTOR BOARD

DIRECTOR BOARD

DIRECTOR BOARD

DIRECTOR BOARD

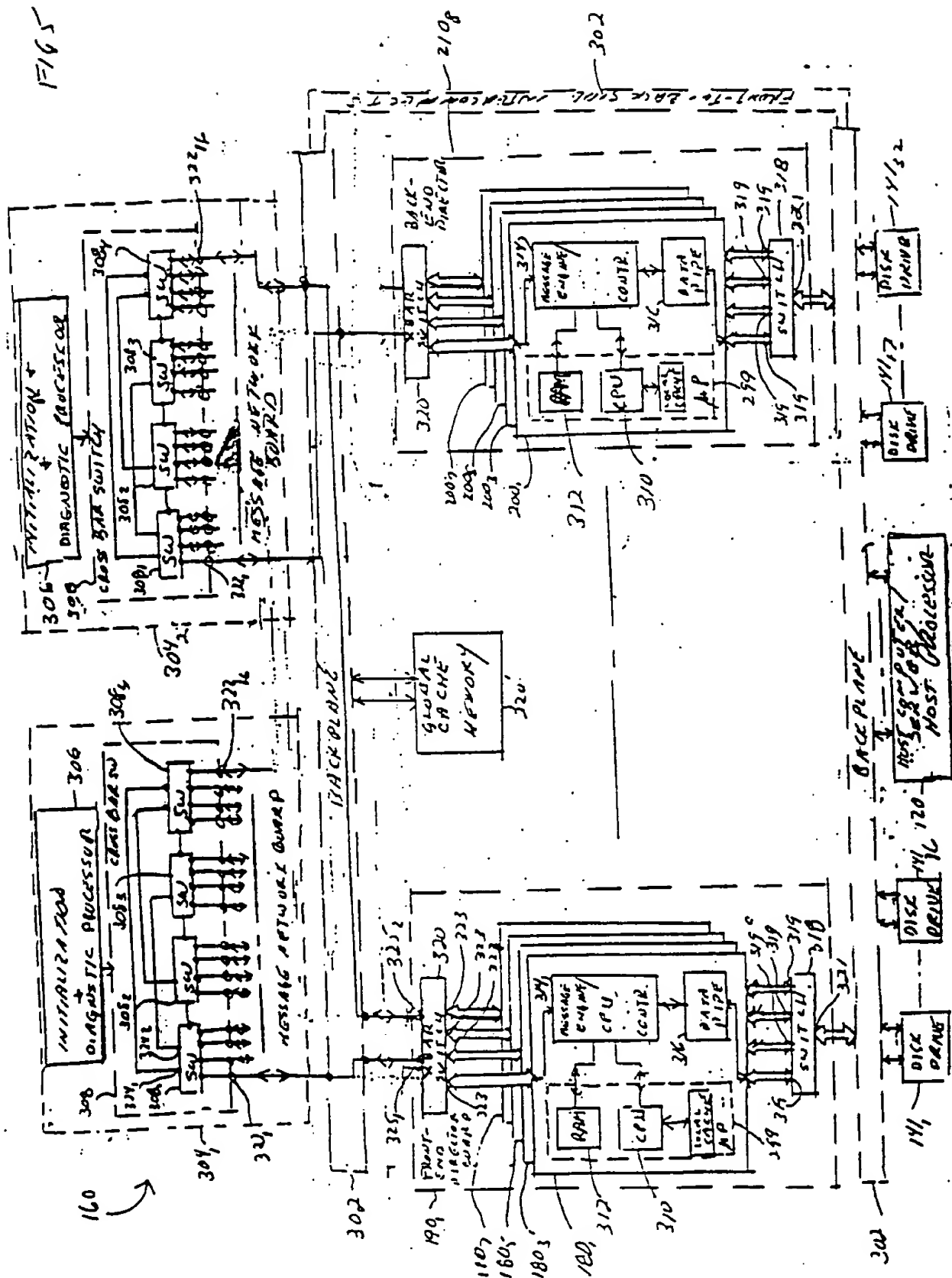
DIRECTOR BOARD

DIRECTOR BOARD

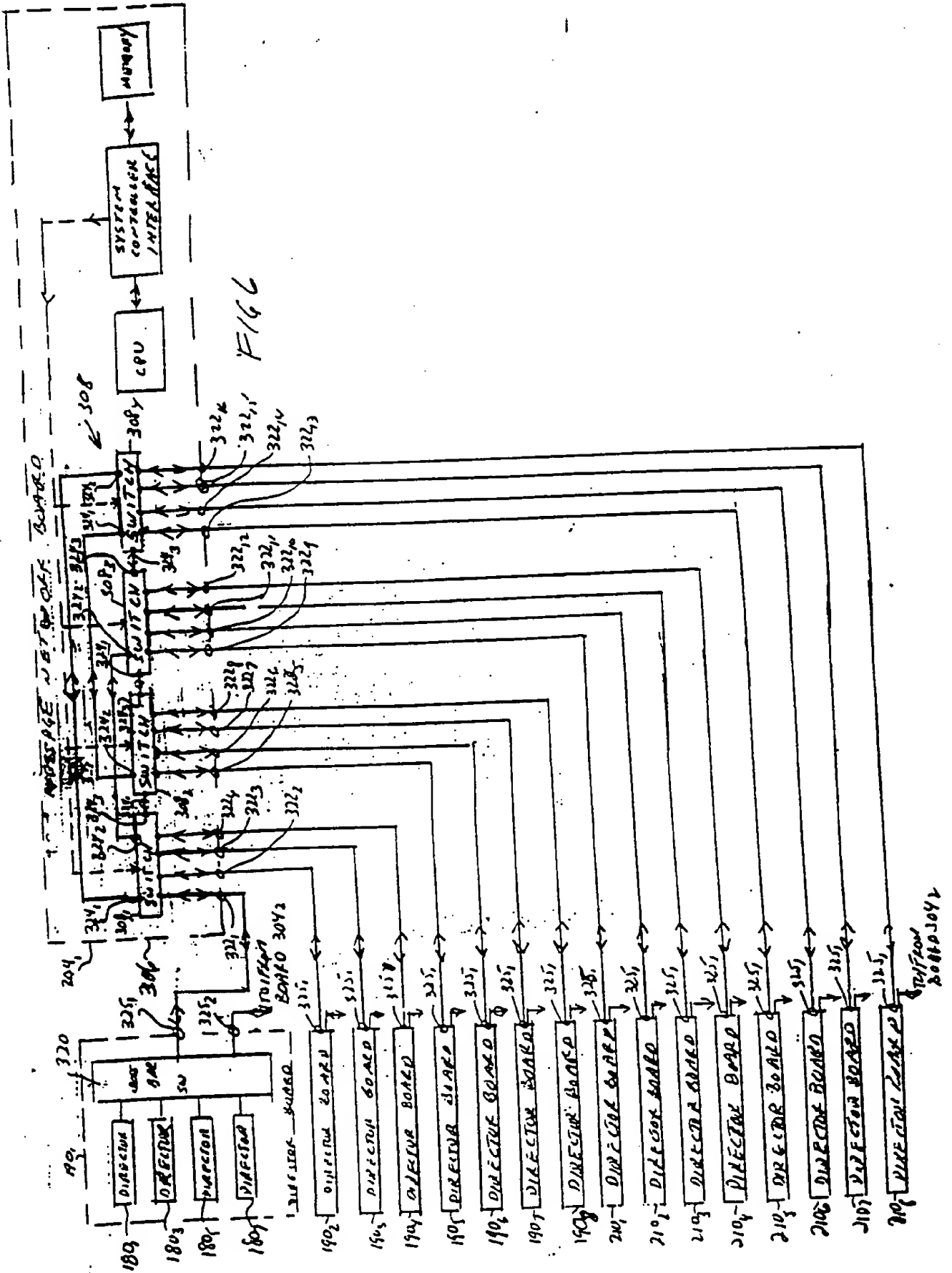
CACHE MEMORY BOARD

BACKPLANE 302

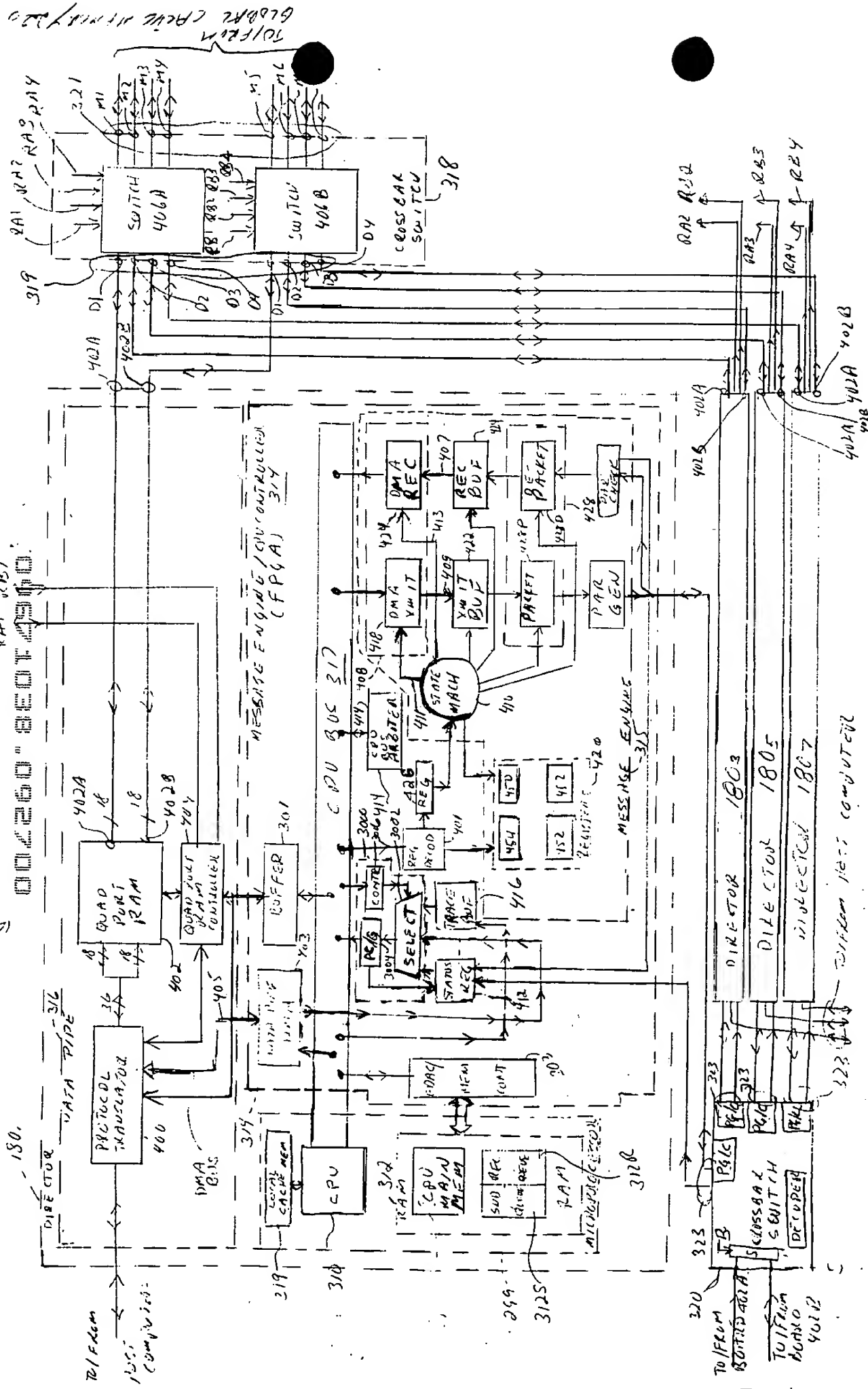
CACHE MEMORY BOARD



002250-092950



002260" BEET 4340.  
RA, 2B1



09571038 092700

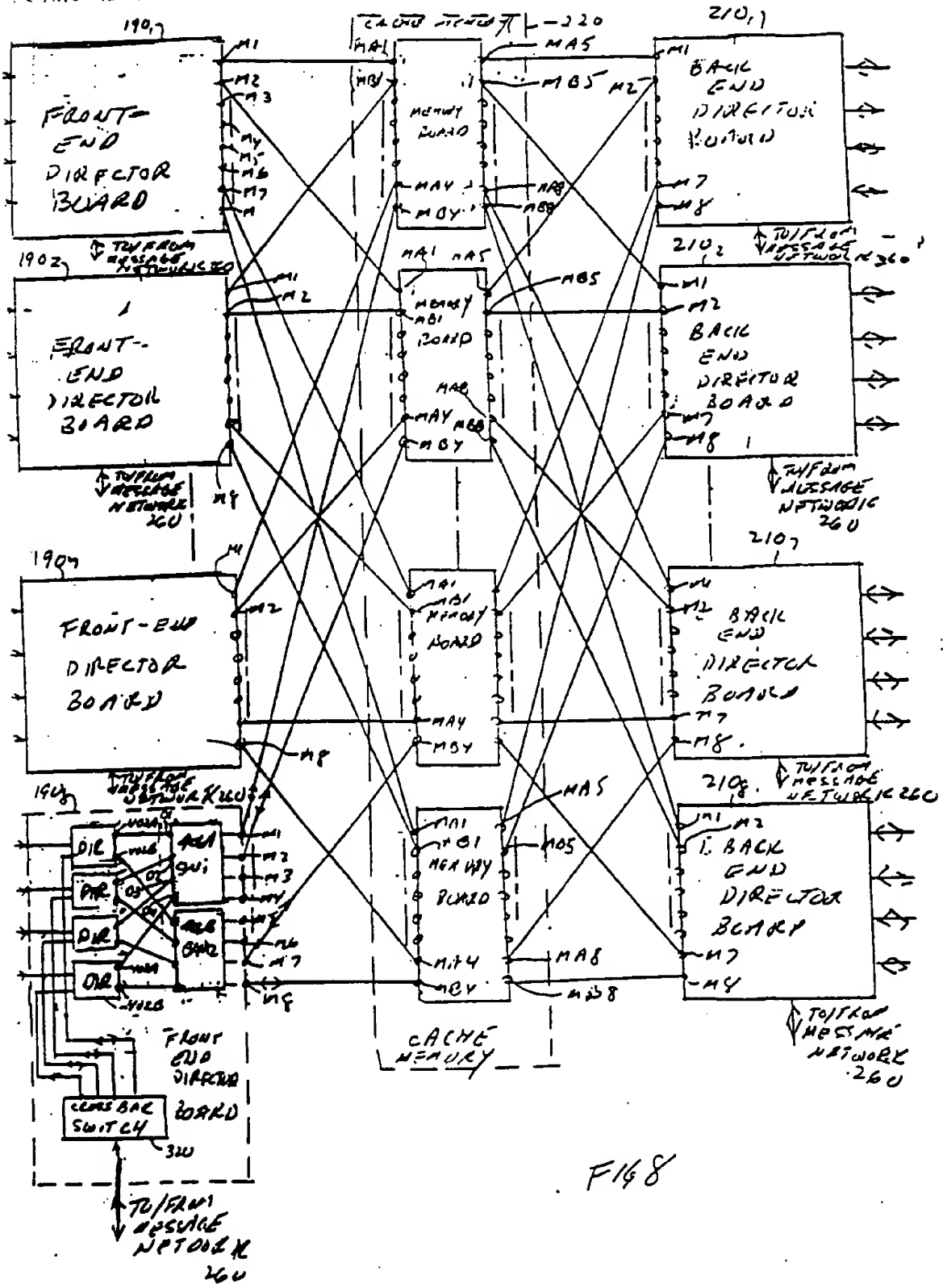


FIG 8



FIG 8A

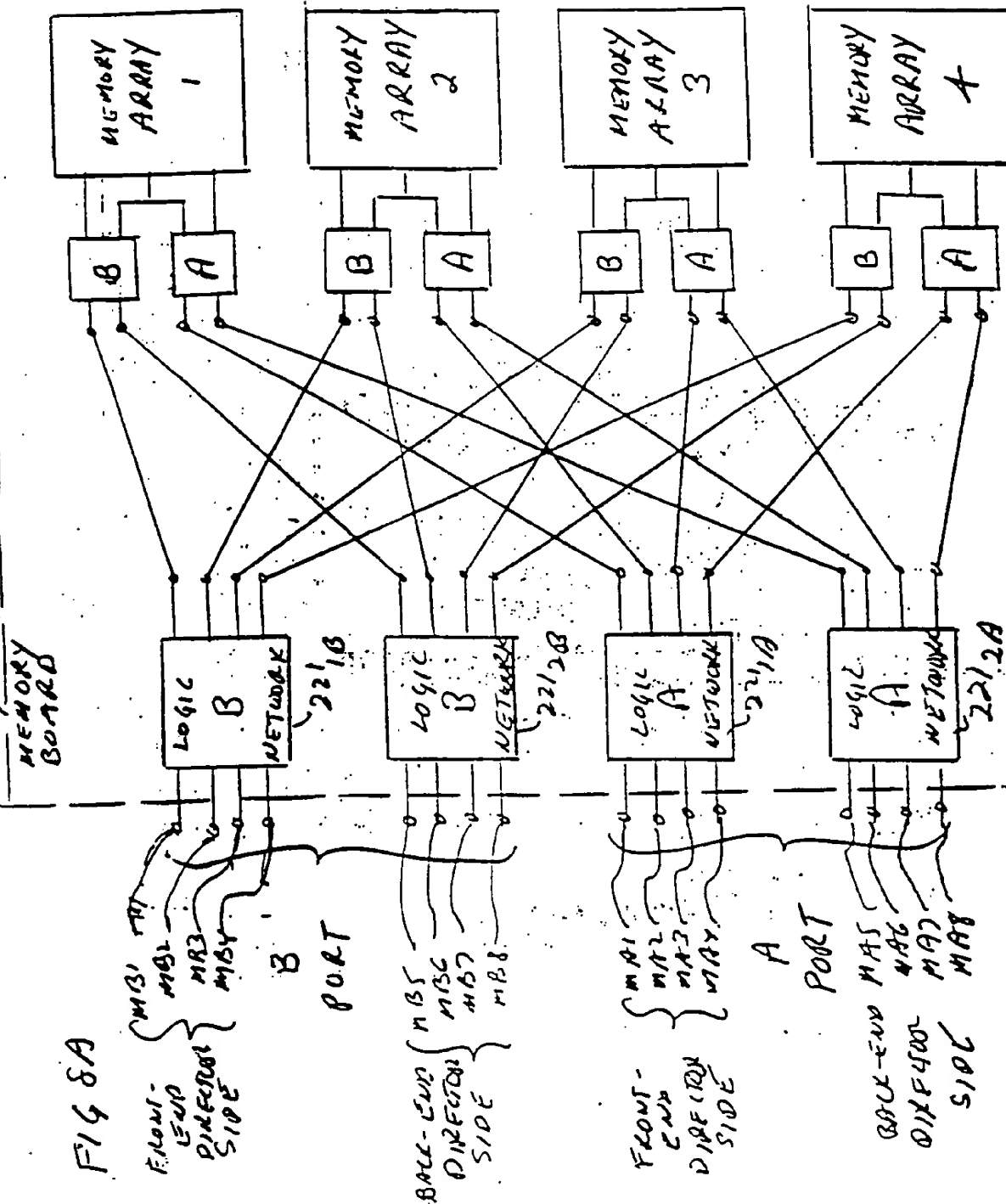


FIG 8B

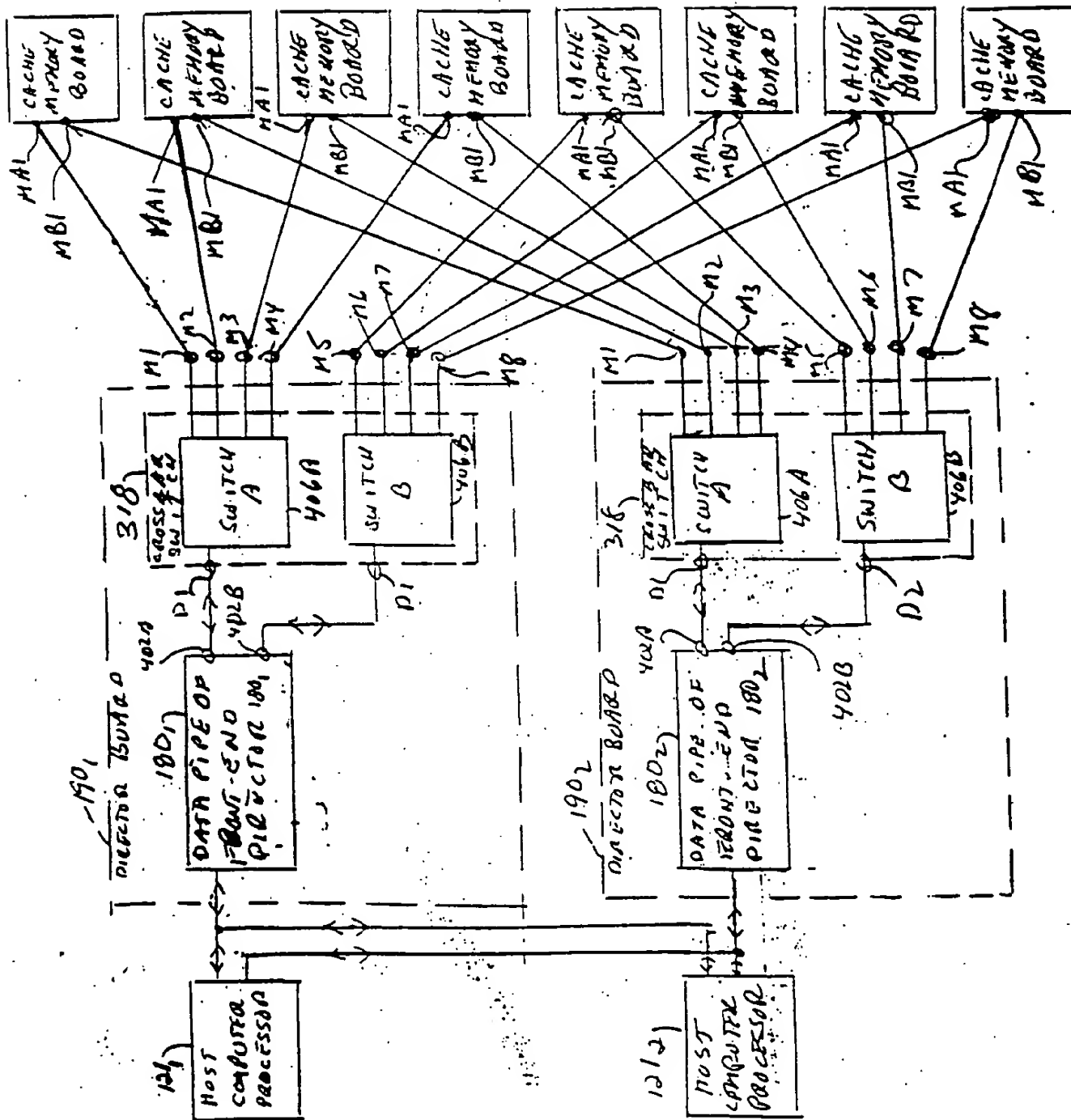
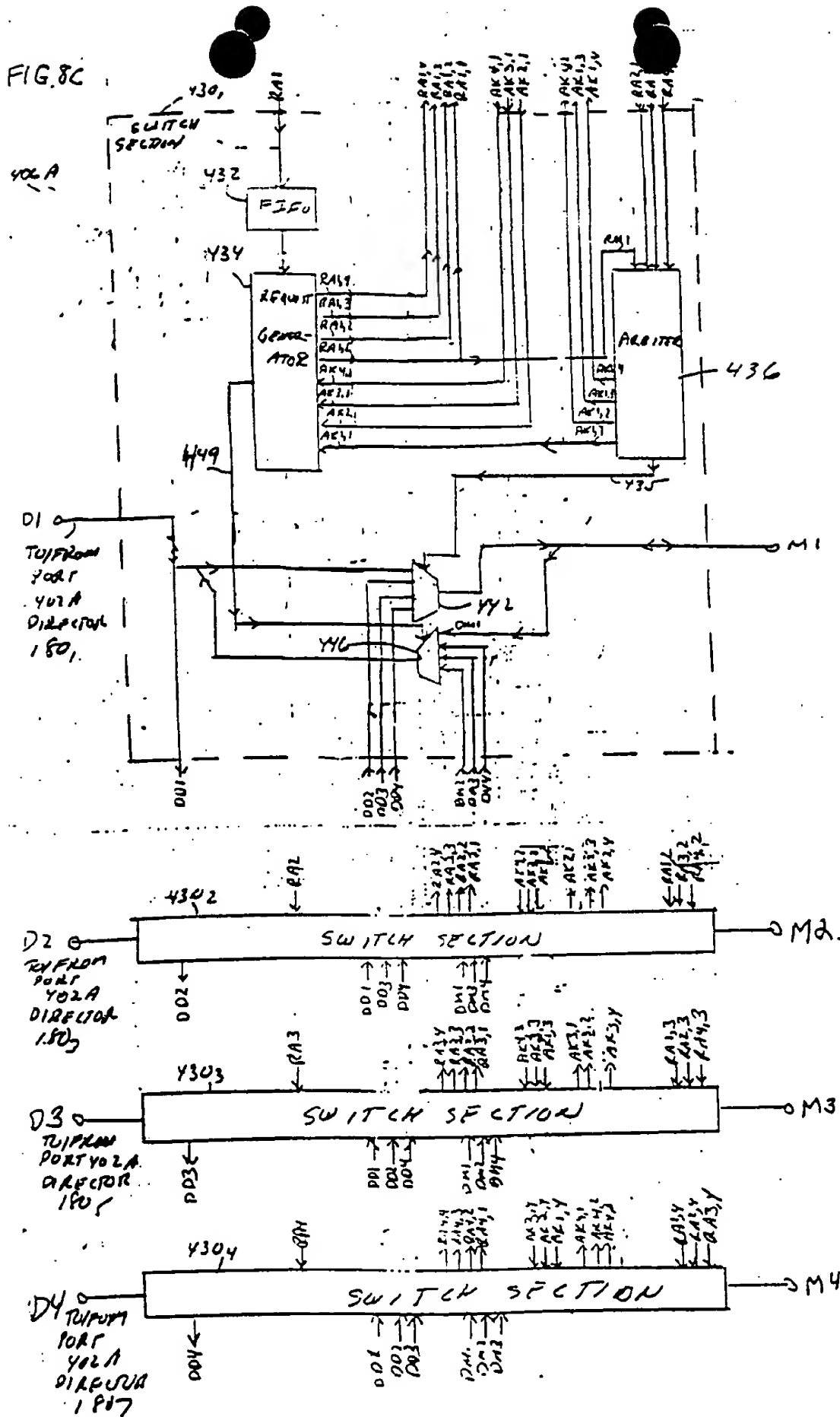
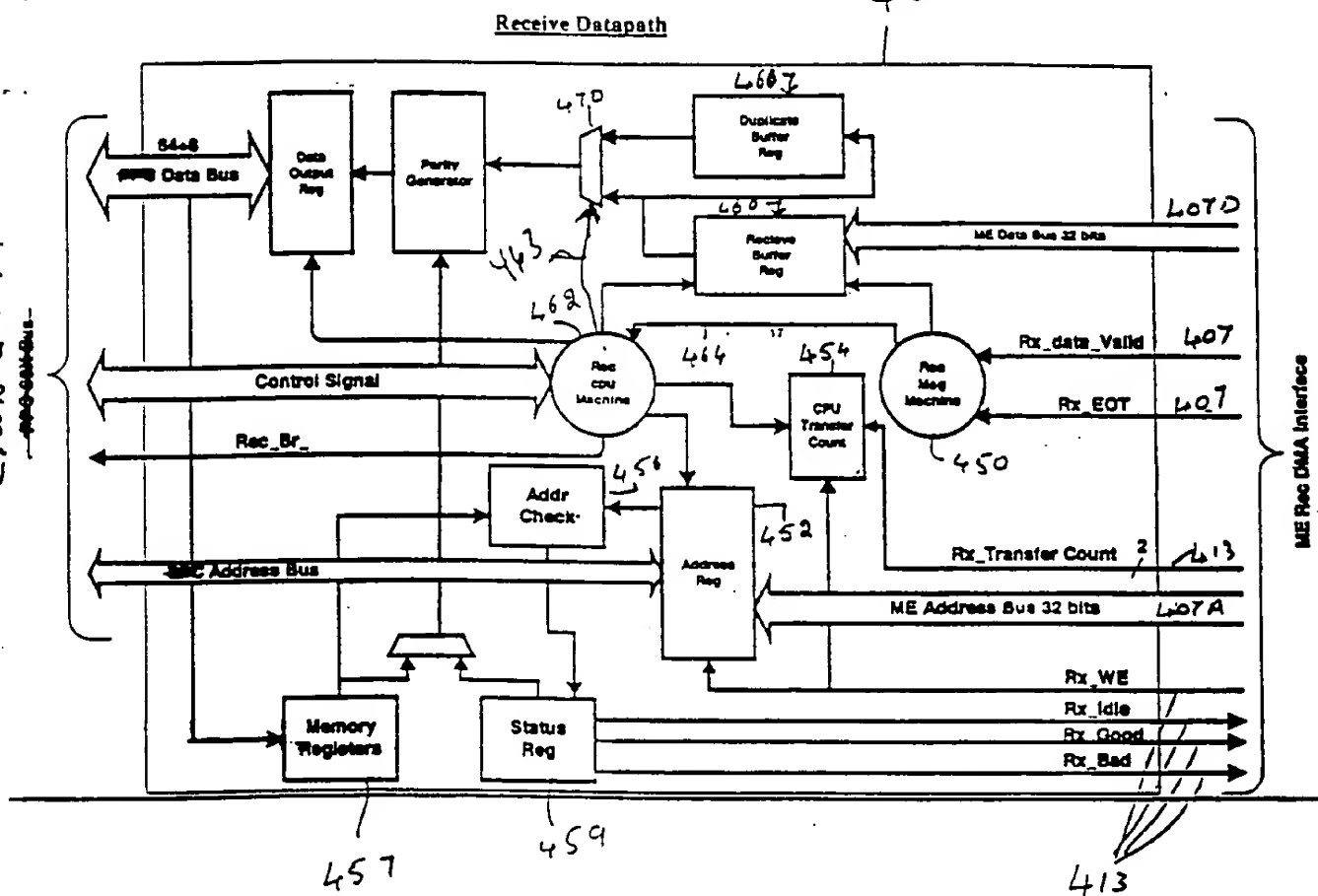


FIG. 8C





420



# Message Bus Send Operation

FIG. 11A

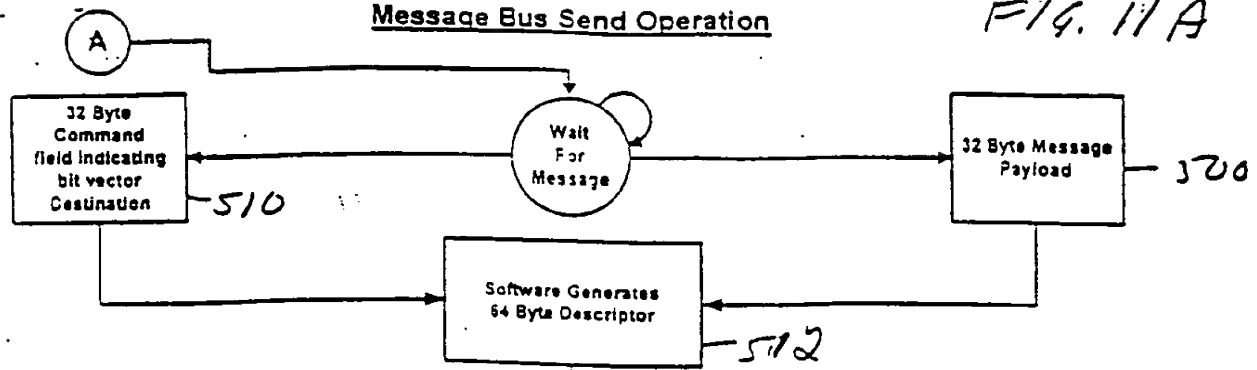


FIG 11

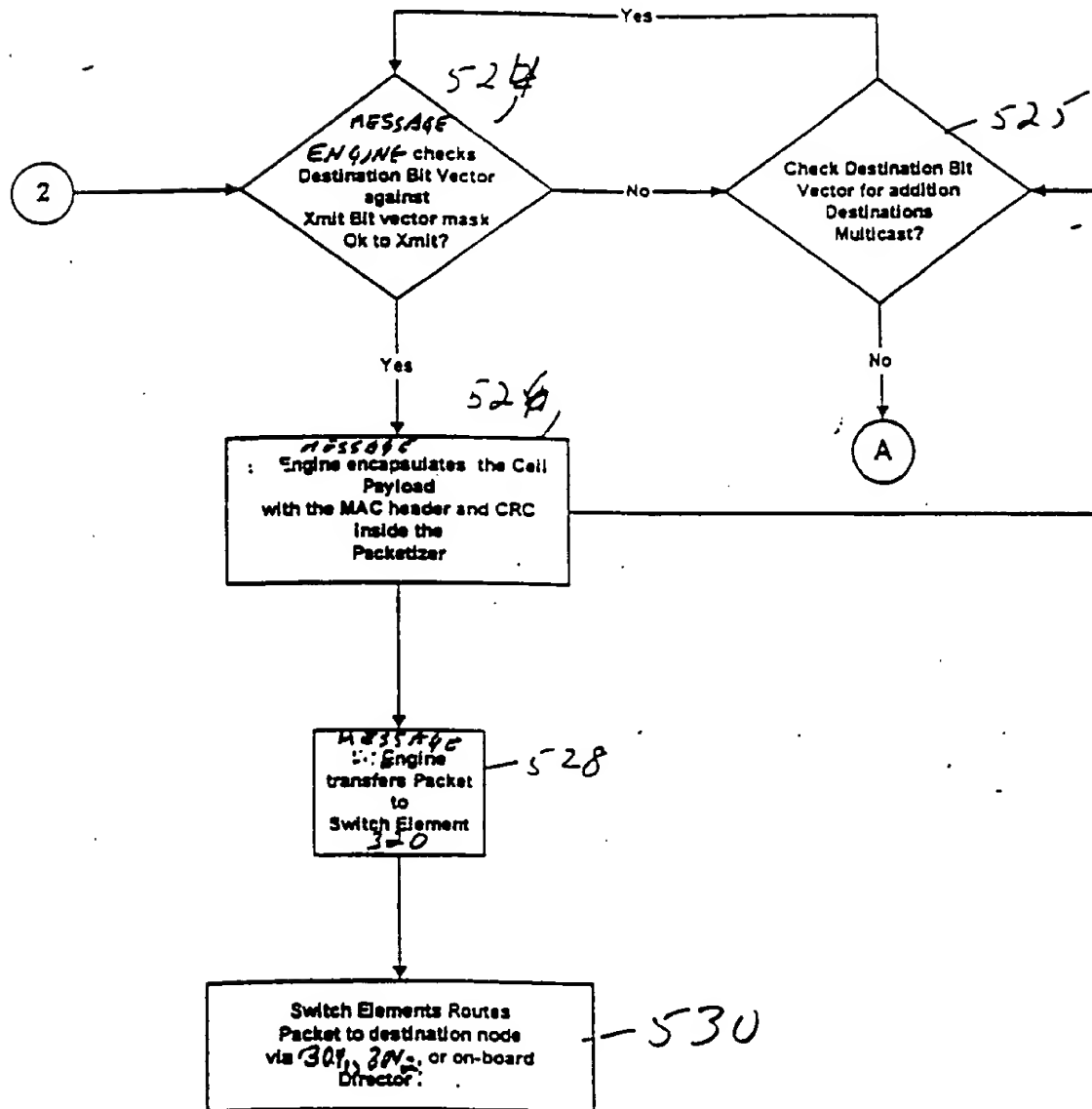
FIG 11A

FIG 11B

00260-092700

Message Bus Send Operation Continued

F1611B



00671038-092700

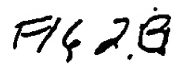
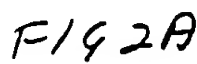
FIG 11c

FIG 11D

FIG 11 E

FLG 11F. MVS

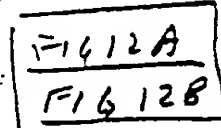
Fig 11G



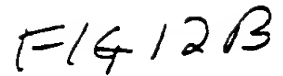


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2025-09-24



F19.13

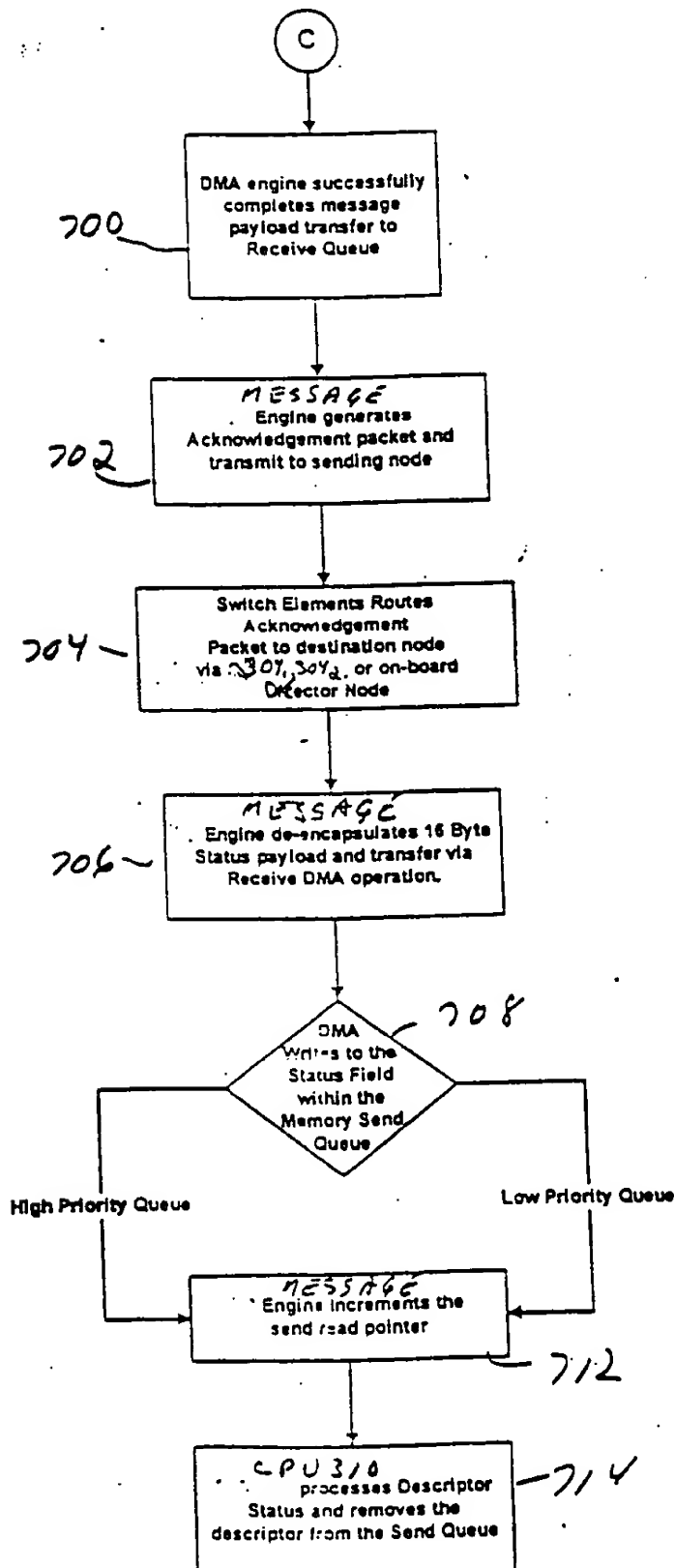
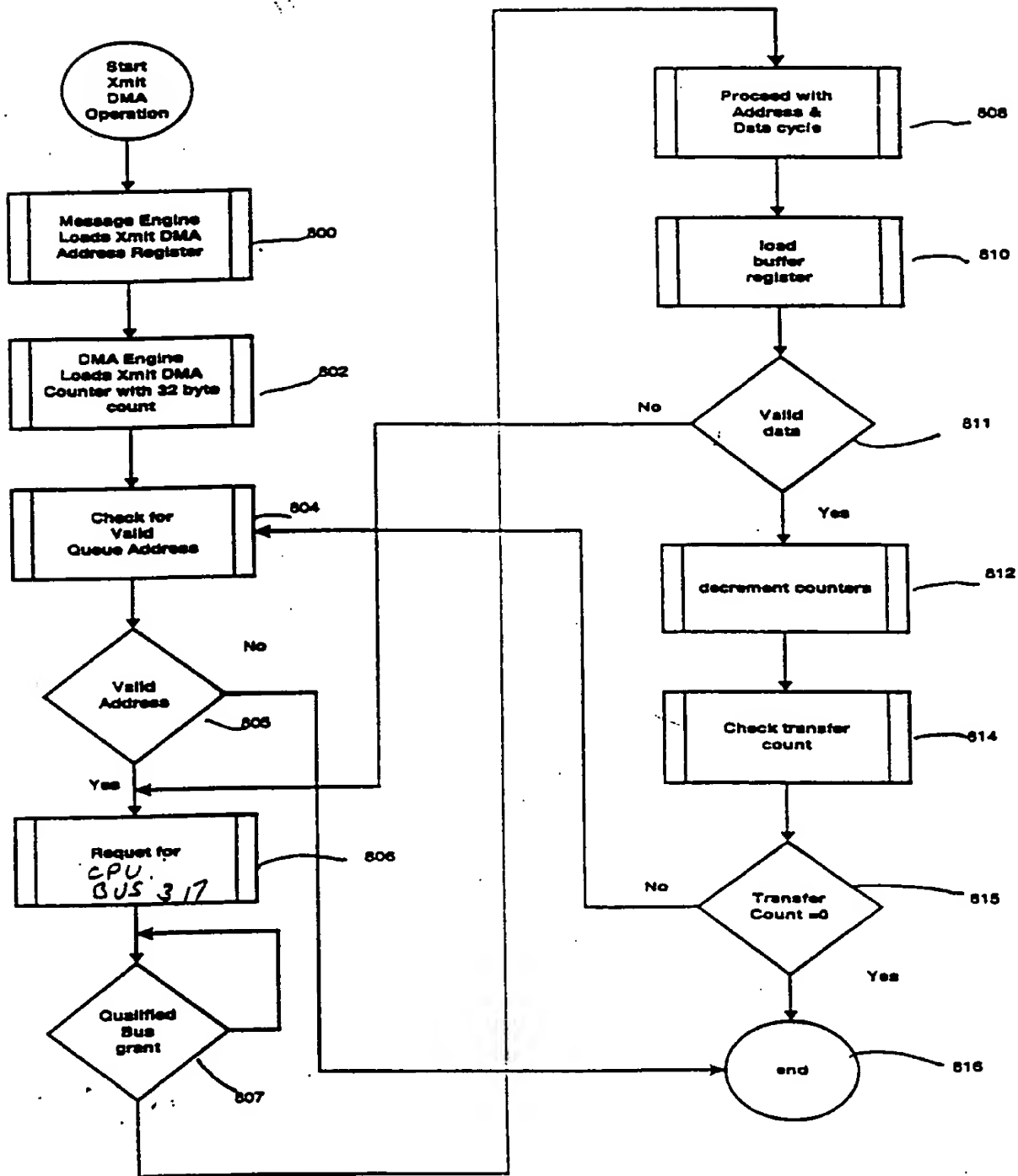


FIG 1A

Xmit CPU flow



002250" BERT 2960

**067100**

### Xmit Msg flow

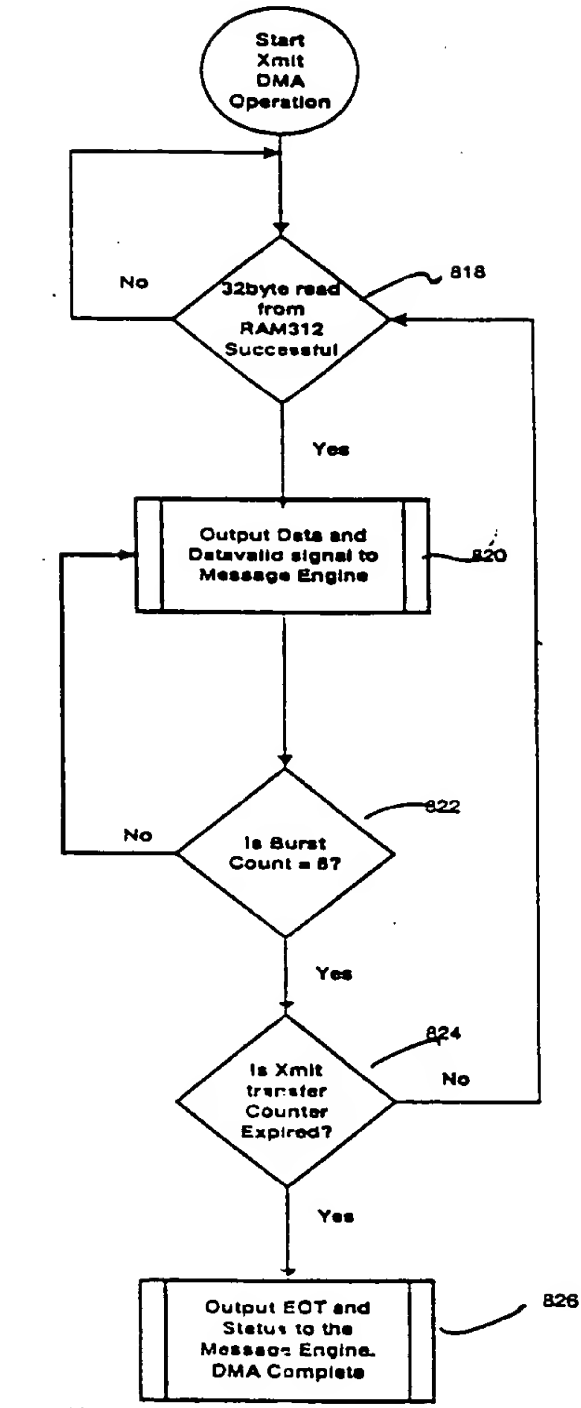
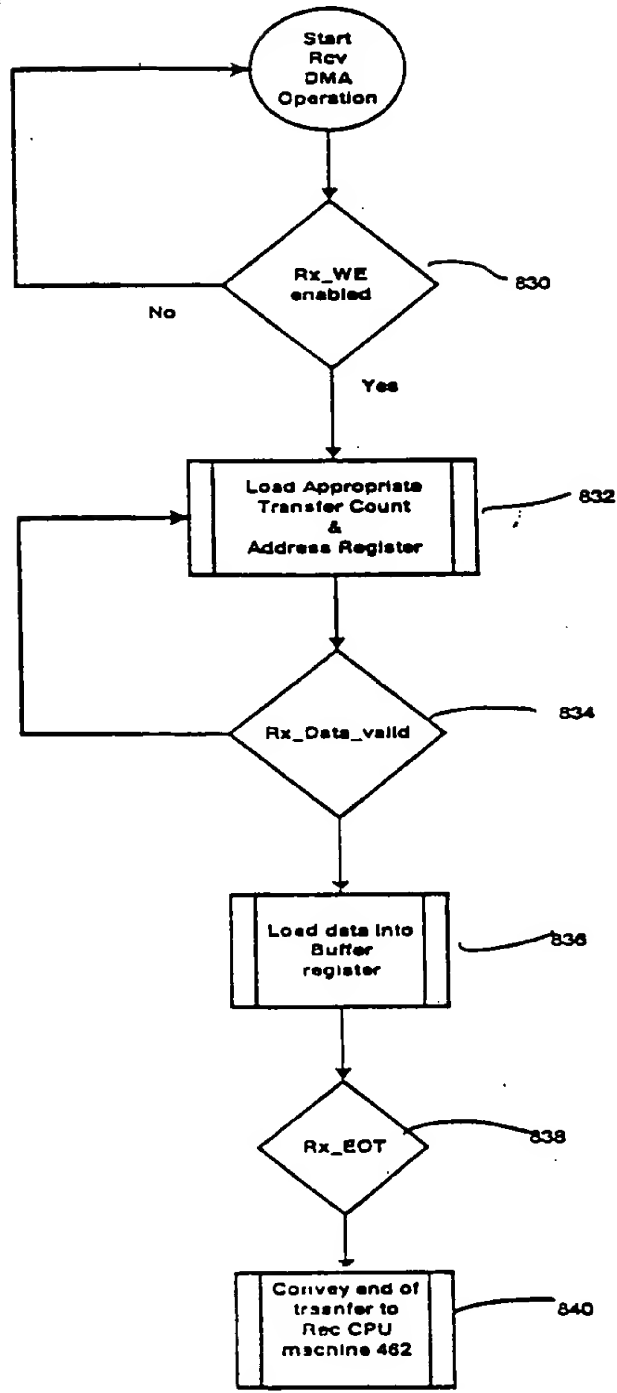


FIG 15A

Rec msg flow



00/260" BEOT/960

F1415B

Rec cpu flow

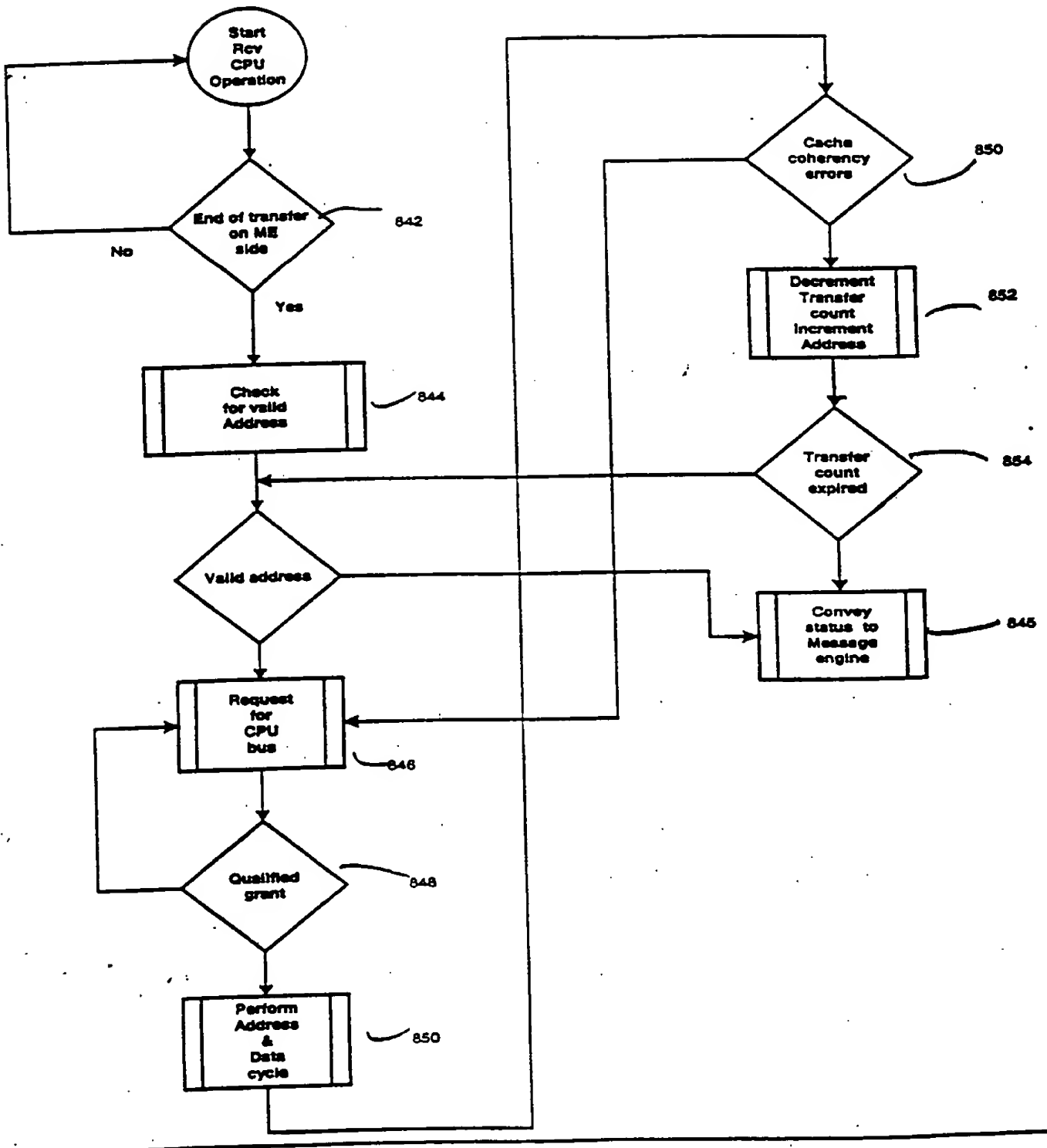


FIG 16

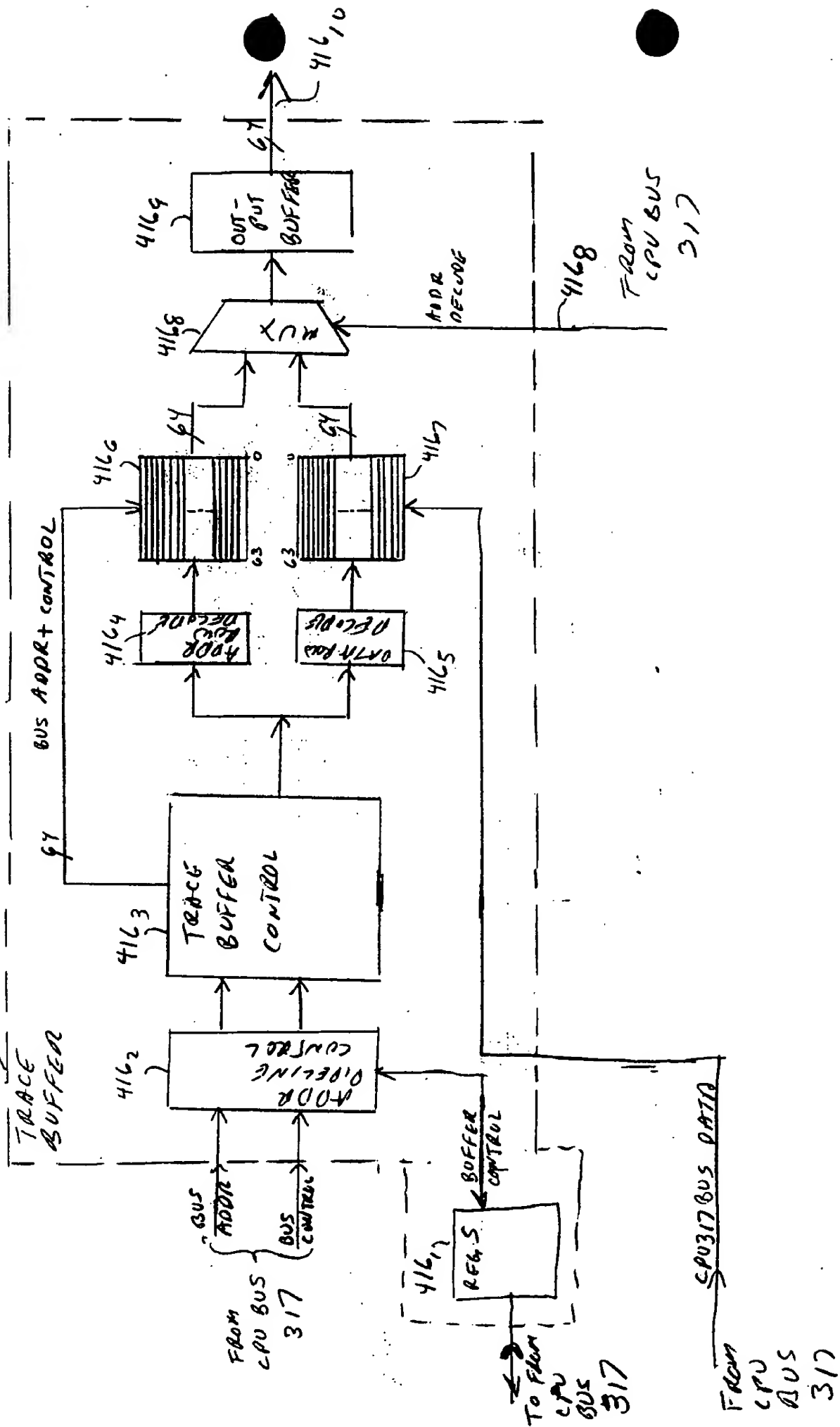
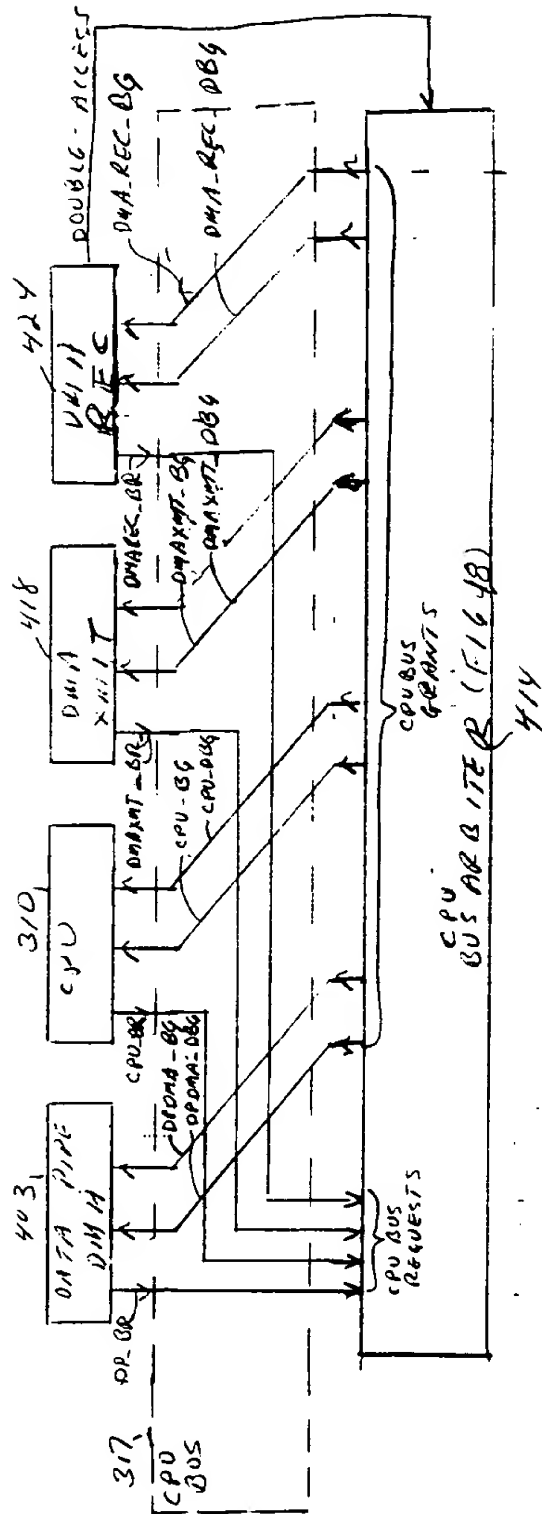




FIG. 17

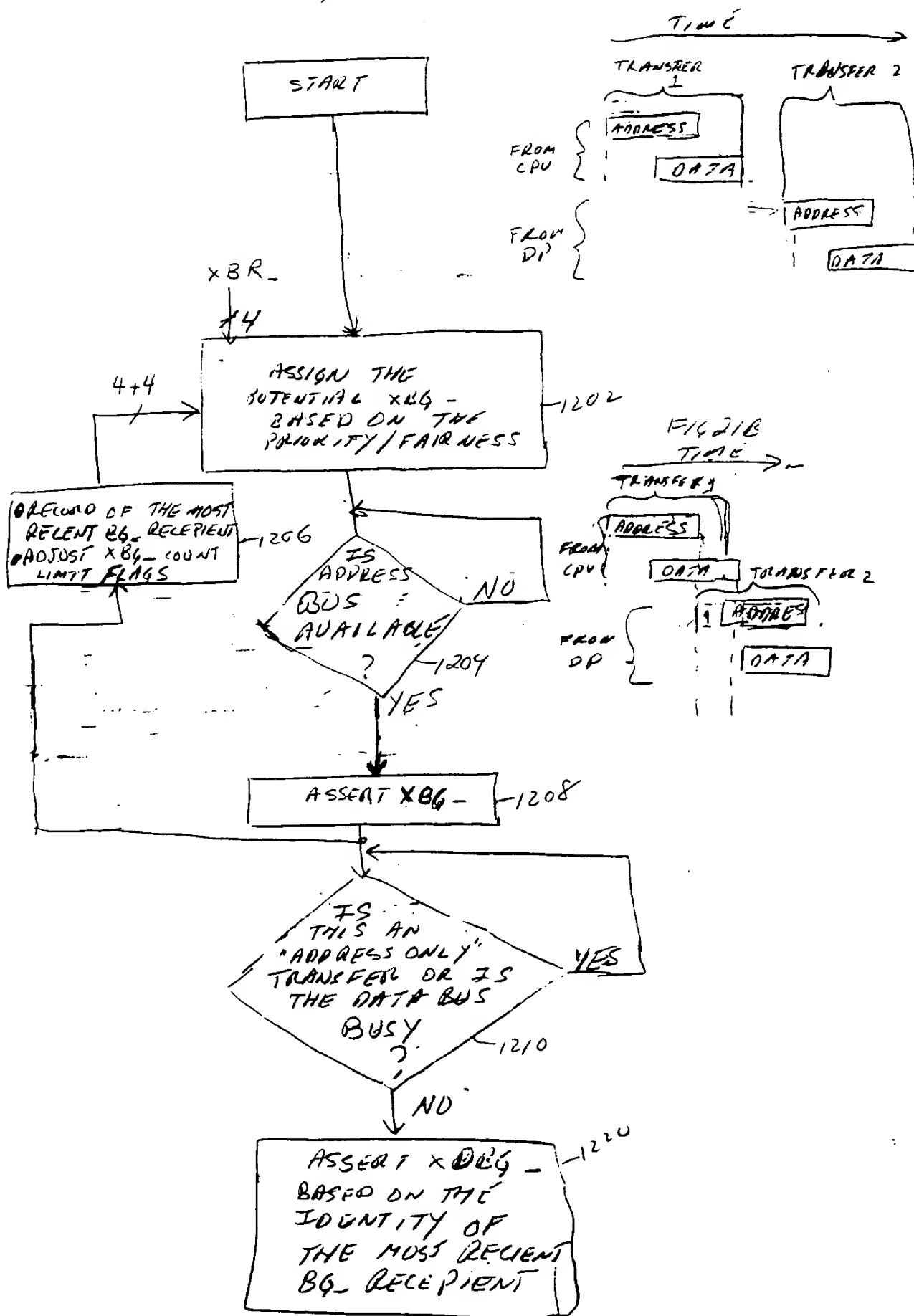




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**0976038**

FIG 2 LA



219  
LOCAL CHINE  
210

DATA FILE  
13MAY 403

